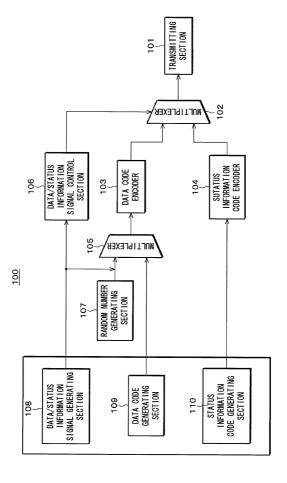
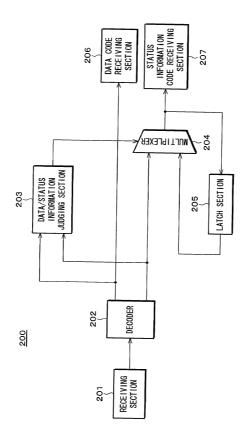
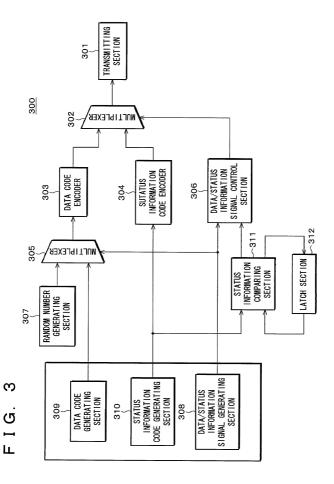
F I G. 1

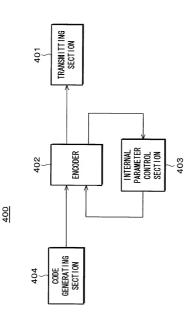


F I G. 2





F I G. 4



DATA/STATUS INFORMATION JUDG ING SECT I ON 504 DECODER 503 500 SYNCHRONIZATION F I G. 5 CIRCUIT 502 믎 RECE IVING SECTION 501

STATUS INFORMATION CODE RECEIVING SECTION

MULTIPLEXER

508

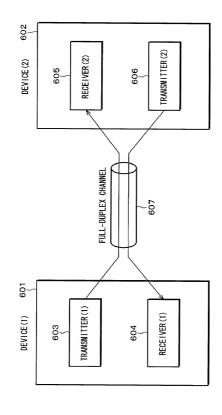
505

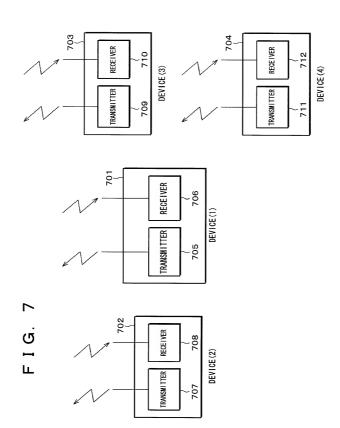
506

LATCH SECTION

DATA CODE RECEIVING SECTION

F I G. 6





F I G. 8

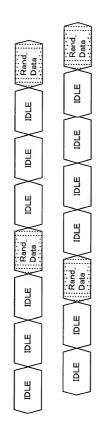


FIG. 9

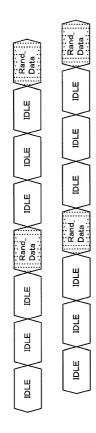
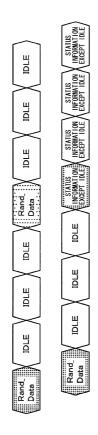
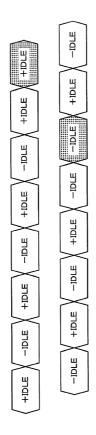


FIG. 10



## F I G. 11



+IDLE:0011111010 -IDLE:1100000101

FIG. 12

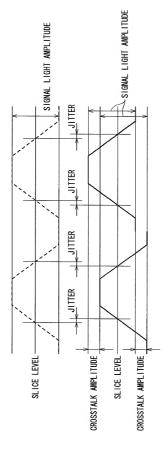


FIG. 13

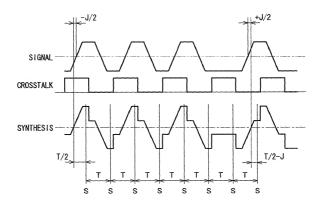


FIG. 14

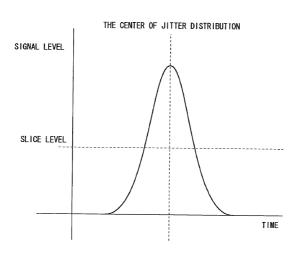


FIG. 15

